Notes:

1. Allow clearance for finger squeeze access from the two sides shown.

2. No tracks or vias in the shaded "KEEP OUT" AREA.

3. No other track or signal within .02" of any contact pad.

4. Do not allow solder paste on the contact pads. It is essential to eliminate any hole in the solder paste-mask (solder stencil) layer. If this is not possible make the contact pads thru-hole with a finished hole size of .008" or less.

5. Leg and alignment holes may be plated and used as vias but ensure minimum finished hole size meets specified tolerances.

6. To prevent purchasing confusion, please specify DNL in your BOM.

7. If using a proto-board fab that plates all through holes, make sure all holes are defined as plated in the footprint.
NOTE:
Dimensions subject to change

TC2030-IDC PLUG-OF-NAILS (TM) CABLE

ZONE A
TC2030-IDC
5/17/11 MS

Detail D

Tag Connect inserted into PCB for programming / debugging

PART NUMBER A
TC2030-IDC 8.8 225
TC2030-IDC-10 10.5 265

END VIEW
END VIEW

TAG 6 PIN IDC 6pin
1 1
2 2
3 3
4 4
5 5
6 6

www.TAG-CONNECT.com SHEET 2 OF 3
Layout Problems to Avoid

Vias between contact pads

Do not deposit solder paste on contact pads. View Solder Paste Mask (Solder Stencil) layer in pad-stack and eliminate any hole.

Traces less than .02" away from contact pad or in "KEEP OUT" area

Large components blocking finger access to the Tag-Connect leg clips