Notes:

1. No tracks or vias in the shaded "KEEP OUT" AREA.
2. No other track or signal within 0.020" of any contact pad.
3. Do not allow solder paste on the contact pads. It is essential to eliminate any hole in the solder paste-mask (solder stencil) layer. If this is not possible make the contact pads thru-hole with finished hole size of 0.008" or less.
4. To avoid ordering confusion, please specify DNL in your BOM.
5. The TC2030-CLIP board can attach to the three alignment pins from underneath the PCB to temporarily retain the cable in place.

Microchip ICD / ICSP signals
Pin 1: MCLR / Vpp
Pin 2: Vdd
Pin 3: GND
Pin 4: PGD (ICSPDAT)
Pin 5: PGC (ICSPCLK)
Pin 6: Unused / LVP
NOTE: Dimensions subject to change

TC2030-MCP-NL PLUG-OF-NAILS™ CABLE

TC2030-MCP-NL
06/19/22
NS

Detail D

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<th>SIGNAL</th>
<th>TAG 6 PIN</th>
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<tr>
<td>GND</td>
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<td>4</td>
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<tr>
<td>PGD (ICSP DAT)</td>
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<td>3</td>
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<tr>
<td>PGC (ICSP CLK)</td>
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<td>NC / LVP</td>
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PART NUMBER | A   | IN MM |
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Vias between contact pads

Detail A

Traces less than .02" away from contact pad or in "KEEP OUT" area

Detail B

Applying solder paste to contact pads because of holes in solder Paste Mask

Detail C