Notes:

1. Allow clearance for finger squeeze access from the two sides shown.

2. No tracks or vias in the shaded "KEEP OUT" AREA.

3. No other track or signal within .02" of any contact pad.

4. Do not allow solder paste on the contact pads. It is essential to eliminate any hole in the solder paste-mask (solder stencil) layer. If this is not possible, make the contact pads thru-hole with a finished hole size of .008" or less.

5. Leg and alignment holes may be plated and used as vias but ensure minimum finished hole size meets specified tolerances.

6. To prevent purchasing confusion, please specify DNL in your BOM.

Microchip ICD / ICSP signals
Pin 1: MCLR / Vpp
Pin 2: Vdd
Pin 3: GND
Pin 4: PGD (ICSPDAT)
Pin 5: PGC (ICSPCLK)
Pin 6: Unused / LVP
NOTE:
Dimensions subject to change

TC2030-MCP PLUG-OF-NAILS (TM) CABLE
TC2030-MCP

PART NUMBER A IN MM
TC2030-MCP 9 228
TC2030-MCP-10 11.7 297

Tag Connect inserted into PCB for programming / debugging

SIGNAL	TAG 6 PIN	RJ12
MCLR / VPP	1	6
VDD	2	5
GND	3	4
PGD(ICSP DAT)	4	3
PGC(ICSP CLK)	5	2
NC / LVP	6	1

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TC2030-MCP
SHEET 2 OF 3
Layout Problems to Avoid

- **Detail A**: Vias between contact pads.
- **Detail B**: Traces less than .02” away from contact pad or in "KEEP OUT" area.
- **Detail C**: Do not deposit solder paste on contact pads. View Solder Paste Mask (Solder Stencil) layer in pad-stack and eliminate any hole.
- **Large components blocking finger access to the Tag-Connect leg clips**.